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#18/Response  
6/9/03  
Shane Th

Attorney Docket no: SEL 137 DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Yamazaki et al.

Serial No.: 09/821,473

Filed: March 29, 2001

For: Semiconductor Device Provided With  
Semiconductor Circuit Made of  
Semiconductor Element and Method Of  
The Same

Examiner: H. Lee

Art Unit: 2823

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: the Assistant Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 27, 2003  
(Date of Deposit)

Shannon Wallace

Name of applicant, assignee, or Registered Rep.

Shannon Wallace 5/27/03  
Signature Date

RESPONSE D

Applicants have the following response to the Office Action of February 27, 2003.

Applicants will address each of the Examiner's rejections in the order in which they appear in the Office Action.

I. Claim Rejections 35 USC §102

In the Office Action, the Examiner rejects Claims 47, 49, 51-53, 56, 57, 59 and 60 under 35 USC §102(e) as being anticipated by Jinno et al. This rejection is respectfully traversed.